



Test report No:

NIE: 2060538R-A332252010

Test Report

LoRa Alliance End Device Certification Requirements

Identification of item tested	LoRaWAN Module
Trademark.....	Murata
Model or type reference.....	LBAA0QB1SJ
Final HW version.....	MP
Final FW version	1.0.01
Final SW version.....	NA
Features.....	LoRa Alliance End Device Certification Requirements for AS 923MHz ISM Band Devices
Manufacturer	Murata Manufacturing Co., Ltd. 10-1, Higashikotari 1-chome, Nagaokakyo-shi, Kyoto 617-8555, Japan
Test method requested.....	LoRa Alliance Certification
Standard.....	LoRa Alliance End Device Certification Requirements for AS 923MHz ISM Band Devices Ver.1.1.1
Test According LoRaWAN™ Spec	V1.0.2
Supported optional features.....	YES
Adaptive Data Rate	YES
→ SF7BW250	YES
→ FSK.....	YES
Test procedure(s).....	TERD-WTS-TP-02 – LORA_TSSTP_PART_1_v1.0
Summary.....	IN COMPLIANCE
Approved by (name / position & signature)	Jimmy Chang Project Manager
Date of issue.....	2021-01-04
Report template No	FLO001_01

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Uncertainty

N/A

Usage of samples

Samples undergoing test have been selected by: SyChip Electronic Technology (Shanghai) Ltd.

DUT Label ID	PSR-1561207
Model or type reference	LBAA0QB1SJ
Serial number	NA
HW version	MP
SW version	NA
FW version	1.0.01
Description of test sample	The Module, part number LBAA0QB1SJ, has been designed to support for operation in EU868, US915, and AS923 bands. Modem Firmware is pre-flashed, so the module can be utilized as small size modem(10.0mm x 8.0mm x 1.6mm(max))and require the Application to run on an external Host.
Date of reception	2020-06-15

Identification of the client

Company name	Murata Electronics Europe
Address	18-22 Avenue Edouard Herriot - Copernic-6, 92356 Le Plessis Robinson Cedex, France

Testing period

Start Date	2020-12-23
Finish Date	2020-12-30

The tests have been performed at DEKRA Testing and Certification, Co., Ltd. (Taiwan)

Environmental conditions

The testing has been performed within the following limits:

Temperature	Min. = 15 °C Max. = 35 °C
Relative humidity	Min. = 20 % Max. = 80 %

Remarks and comments

Test Report Creator	Gavin Yang gavin.yang@dekra.com
Test Engineer	Gavin Yang gavin.yang@dekra.com

Revision History

Revision	Modification Date	Description
01	NA	Initial Report

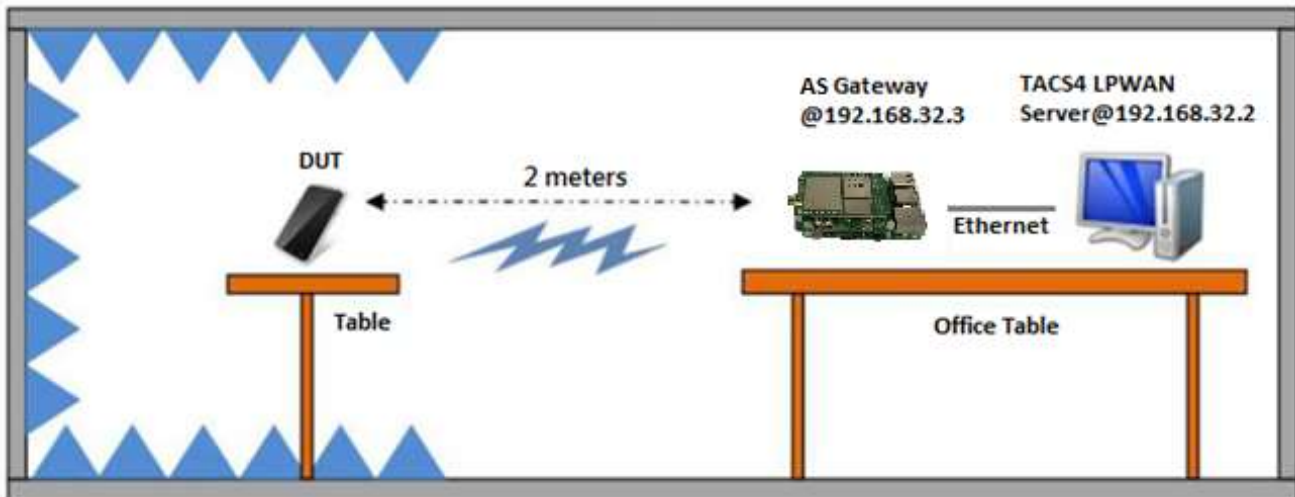
Means of testing identification

Following equipment was used to perform the testing:

AS923 SETUP			
Test System	CN. 556	TACS4 LPWAN Testing, Approvals & Certification System	
Hardware:	Control No.	Equipment	Serial No.
	0596	LoRa Gateway RISINGHF - RHF2S001	E305654
Software:	0559	TACS4 LORA GUI v1.17.0 TACS4 LORA Reporting Module v1.6.0	
	0560	TACS4 LORA Technology Package v5.21.0_R1	

Test setup

The configuration used for Test Cases in nominal temperature conditions was the following one:



Appendix A – Test result

The abbreviations used in the header row of the test campaign report tables are:

- Test Case ID: Test case identifier, as it can be found on the referred standard.
- Sample: Sample details.
- Description: Test case description, as it can be found on the referred standard.
- Date: Date of the beginning of the execution.
- Conformance: YES/NO. If the test case has been executed in accordance to the standard.
- Verdict: Records the verdict assigned to each Test case run to completion. Following verdicts are possible:
 - PASS**: If the Test case passed.
 - FAIL**: If the Test case failed.
 - INCONC**: Inconclusive. The test case did not reach a PASS or FAIL verdict.
 - NA**: Not applicable.
 - NM**: Not measured.

Observations: Provides a reference to additional information relevant to the test (when required).

Test Case ID	Description	Verdict	Date	Observations
TP_A_AS923_ED_MAC_BV_000	Test Mode Activation	Pass	2020-12-30	ABP
TP_A_AS923_ED_MAC_BV_001	Test Application functionality	Pass	2020-12-30	ABP
TP_A_AS923_ED_MAC_BV_003	Downlink Error Rate	Pass	2020-12-30	ABP
TP_A_AS923_ED_MAC_BV_004	AES encryption and message integrity	Pass	2020-12-30	ABP
TP_A_AS923_ED_MAC_BV_005	Downlink window timing	Pass	2020-12-30	ABP
TP_A_AS923_ED_MAC_BV_006	Frame sequence number	Pass	2020-12-30	ABP
TP_A_AS923_ED_MAC_BV_007	DevStatusReq MAC command	Pass	2020-12-30	ABP
TP_A_AS923_ED_MAC_BV_008	MAC Commands	Pass	2020-12-30	ABP
TP_A_AS923_ED_MAC_BV_009	NewChannelReq MAC command	Pass	2020-12-30	ABP
TP_A_AS923_ED_MAC_BV_010	DIChannelReq MAC command	Pass	2020-12-30	ABP
TP_A_AS923_ED_MAC_BV_011	Confirmed packets	Pass	2020-12-30	ABP
TP_A_AS923_ED_MAC_BV_012	RXParamSetupReq MAC command	Pass	2020-12-30	ABP
TP_A_AS923_ED_MAC_BV_013	RXTimingSetupReq MAC command	Pass	2020-12-30	ABP
TP_A_AS923_ED_MAC_BV_014_A	LinkADDRReq MAC command	Pass	2020-12-30	ABP
TP_A_AS923_ED_MAC_BV_014_B	LinkADDRReq MAC command	Pass	2020-12-30	ABP
TP_A_AS923_ED_MAC_BV_015	Packet Error Rate RX1	Pass	2020-12-30	ABP
TP_A_AS923_ED_MAC_BV_016	Packet Error Rate RX2	Pass	2020-12-30	ABP
TP_A_AS923_ED_MAC_BV_017	TXParamSetupReq MAC command	Pass	2020-12-30	ABP

Test Case ID	Description	Verdict	Date	Observations
TP_A_AS923_ED_MAC_BV_000	Test Mode Activation	Pass	2020-12-30	OTAA
TP_A_AS923_ED_MAC_BV_001	Test Application functionality	Pass	2020-12-30	OTAA
TP_A_AS923_ED_MAC_BV_002	Over The Air activation	Pass	2020-12-30	OTAA
TP_A_AS923_ED_MAC_BV_003	Downlink Error Rate	Pass	2020-12-30	OTAA
TP_A_AS923_ED_MAC_BV_004	AES encryption and nessage integrity	Pass	2020-12-30	OTAA
TP_A_AS923_ED_MAC_BV_005	Downlink window timing	Pass	2020-12-30	OTAA
TP_A_AS923_ED_MAC_BV_006	Frame sequence number	Pass	2020-12-30	OTAA
TP_A_AS923_ED_MAC_BV_007	DevStatusReq MAC command	Pass	2020-12-30	OTAA
TP_A_AS923_ED_MAC_BV_008	MAC Commands	Pass	2020-12-30	OTAA
TP_A_AS923_ED_MAC_BV_009	NewChannelReq MAC command	Pass	2020-12-30	OTAA
TP_A_AS923_ED_MAC_BV_010	DIChannelReq MAC command	Pass	2020-12-30	OTAA
TP_A_AS923_ED_MAC_BV_011	Confirmed packets	Pass	2020-12-30	OTAA
TP_A_AS923_ED_MAC_BV_012	RXParamSetupReq MAC command	Pass	2020-12-30	OTAA
TP_A_AS923_ED_MAC_BV_013	RXTimingSetupReq MAC command	Pass	2020-12-30	OTAA
TP_A_AS923_ED_MAC_BV_014_A	LinkADRRReq MAC command	Pass	2020-12-30	OTAA
TP_A_AS923_ED_MAC_BV_014_B	LinkADRRReq MAC command	Pass	2020-12-30	OTAA
TP_A_AS923_ED_MAC_BV_015	Packet Error Rate RX1	Pass	2020-12-30	OTAA
TP_A_AS923_ED_MAC_BV_016	Packet Error Rate RX2	Pass	2020-12-30	OTAA
TP_A_AS923_ED_MAC_BV_017	TXParamSetupReq MAC command	Pass	2020-12-30	OTAA

Appendix B – ICS

Item	Name	Value
1	DUT works in AS 923MHz ISM Band	True
2	DUT implements LoRaWAN v1.0.1 certification requirements	False
3	DUT implements LoRaWAN v1.0.2rB certification requirements	True
4	DUT is a Class A Device (All End Device)	True
5	DUT is a Class B Device (Beacon Mode)	False
6	DUT is a Class C Device (Continuously Listening)	False
7	DUT supports Adaptive Data Rate (ADR) feature	True
8	DUT supports LinkADRReq block	True
9	DUT supports DlChannelReq MAC command	True
10	DUT supports Over-The-Air Activation (OTAA) mechanism	True
11	DUT needs a reset after deactivating Test Mode	True
12	DUT supports Trigger Join Request command in Test Mode	True

Appendix C – IXIT

Item	Name	Value
ABP	End-Device Address (DvAddr)	08159D03
	Application Session Key (AppSKey)	2B7E151628AED2A6ABF7158809CF4F3C
	Network Session Key (NwkSKey)	2B7E151628AED2A6ABF7158809CF4F3C
ED	Application Identifier (AppEUI)	0101010101010101
	Maximum Transmission Power	16 dBm
	Mininum Transmission Power	2 dBm
OTAA	Application Key (AppKey)	000102030405060708090A0B0C0D0E0F
	Device Identifier (DevEUI)	3633323552388116

Appendix D – General Parameters

Item	Name	Value
GW	Default TX Antenna	0
	Gateway IP Address	192.168.32.3
	Gateway Model	RISINGHF
	Default TX Power	14 dBm
	Gateway Socket Port	1780
TM - AS 923	AS923 Join_Accept_Delay1	5.0 s
	AS923 Join_Accept_Delay2	6.0 s
	AS923 Receive_Delay1	1.0 s
	AS923 Receive_Delay2	2.0 s
	AS923 RX2 Receive Window DR	SF10BW125
	AS923 RX2 Receive Window Frequency	923.2 MHz
	General Timer	60 s